

**Amendments to the Specification**

Please add the following new paragraph starting on page 1, line 4, before “TECHNICAL FIELD.”

**CROSS REFERENCE TO RELATED APPLICATIONS**

This application is a 35 U.S.C. §371 National Stage filing of International Patent Application No. PCT/US04/42880, titled CONTROLLING REMOVAL RATE UNIFORMITY OF AN ELECTROPOLISHING PROCESS IN INTEGRATED CIRCUIT FABRICATION, filed December 17, 2004; which claims the benefit of U.S. Serial No. 60/587,637, titled CONTROLLING REMOVAL RATE UNIFORMITY OF AN ELECTROPOLISHING PROCESS IN INTEGRATED CIRCUIT FABRICATION, filed July 13, 2004; which claims the benefit of U.S. Serial No. 60/530,385, titled CONTROLLING REMOVAL RATE UNIFORMITY OF AN ELECTROPOLISHING PROCESS IN INTEGRATED CIRCUIT FABRICATION, filed December 17, 2003; both of which are incorporated herein by reference in their entireties.